POWER QUALITY IMPROVEMENT BY USING ACTIVE POWER FILTERS

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Abstract
This paper describes different power quality problems in distribution systems and their solutions with power electronics based equipment. Shunt, hybrid and series active power filters are described showing their compensation characteristics and principles of operation. Different power circuits topologies and control scheme for each type of active power filter are analyzed. The compensation characteristics of each topology with the respective control scheme are proved by simulation and experimentally.

Index Terms: Power Quality Improvement, Active Power Filters, Power Electronics, Distribution Systems

1. INTRODUCTION
The proliferation of microelectronics processors in a wide range of equipments, from home VCRs and digital clocks to automated industrial assembly lines and hospital diagnostics systems has increased the vulnerability of such equipment to power quality problems. These problems include a variety of electrical disturbances, which may originate in several ways and have different effects on various kinds of sensitive loads. What were once considered minor variations in power, usually unnoticed in the operation of conventional equipment, may now bring whole factories to standstill. As a result of this vulnerability, increasing numbers of industrial and commercial facilities are trying to protect themselves by investing in more sophisticated equipment to improve power quality. Moreover, the proliferation of non-linear loads with large rated power has increased the contamination level in voltages and currents waveforms, forcing to improve the compensation characteristics required to satisfy more stringent harmonics standard. Between the different technical options available to improve power quality, active power filters have proved to be an important alternative to compensate for current and voltage disturbances in power distribution systems. Different active power filters topologies have been presented in the technical literature, and many of them are already available in the market. This paper will focus in the analysis of which to use with their compensation characteristics. Shunt active power filters, series active topologies, and hybrid schemes will be presented and analyzed. The control scheme characteristics for shunt and series schemes will also be discussed. Finally, steady state and transient results for dynamic compensation, obtained from simulated and experimental setup will be presented.

2. POWER QUALITY IN DISTRIBUTION SYSTEMS
Most of the more important international standards define power quality as the physical characteristics of the electrical supply provided under normal operating conditions that do not disrupt or disturb the customer’s processes. Therefore, a power quality problem exists if any voltage, current or frequency deviation results in a failure or in a bad operation of customer’s equipment. However, it is important to notice that the quality of power supply implies basically voltage quality and supply reliability. Voltage quality problems relate to any failure of equipment due to deviations of the line voltage from its nominal characteristics, and the supply reliability is characterized by its adequacy and availability.

Power quality problems are common in most of commercial, industrial and utility networks. Natural phenomena, such as lightning are the most frequent cause of power quality problems. Switching phenomena resulting in oscillatory transients in the electrical supply, for example when
capacitors are switched, also contribute substantially to power quality disturbances. Also, the connection of high power non-linear loads contributes to the generation of current and voltage harmonic components. Between the different voltage disturbances that can be produced, the most significant and critical power quality problems are voltage sags due to the high economical losses that can be generated. Short-term voltage drops (sags) can trip electrical drives or more sensitive equipment, leading to costly interruptions of production.

For all these reasons, from the consumer point of view, power quality issues will become an increasingly important factor to consider in order to satisfy good productivity. On the other hand, for the electrical supply industry, the quality of power delivered will be one of the distinguishing factors for ensuring customer loyalty in this very competitive and deregulated market. To address the needs of energy consumers trying to improve productivity through the reduction of power quality related process stoppages and energy suppliers trying to maximize operating profits while keeping customers satisfied with supply quality, innovative technology provides the key to cost-effective power quality enhancements solutions. However, with the various power quality solutions available, the obvious question for a consumer or utility facing a particular power quality problem is which equipment provides the better solution.

3. SOLUTION TO POWER QUALITY PROBLEM

There are two approaches to the mitigation of power quality problems. The first approach is called load conditioning, which ensures that the equipment is less sensitive to power disturbances, allowing the operation even under significant voltage distortion. The other solution is to install line conditioning systems that suppress or counteracts the power system disturbances.

A flexible and versatile solution to voltage quality problems is offered by active power filters. Currently they are based on PWM converters and connect to low and medium voltage distribution system in shunt or in series. Series active power filters must operate in conjunction with shunt passive filters in order to compensate load current harmonics. Shunt active power filters operate as a controllable current source and series active power filters operates as a controllable voltage source. Both schemes are implemented preferable with voltage source PWM inverters, with a dc bus having a reactive element such as a capacitor. Active power filters can perform one or more of the functions required to compensate power systems and improving power quality. As it will be illustrated in this paper, their performance depends on the power rating and the speed of response. The selection of the type of active power filter to improve power quality depends on the source of the problem as can be seen in Table 1.

<table>
<thead>
<tr>
<th>Active Filter</th>
<th>Load on AC Supply</th>
<th>AC Supply on Load</th>
</tr>
</thead>
<tbody>
<tr>
<td>Shunt</td>
<td>Clipped Harmonic Filtering</td>
<td>-Voltage sag/swell.</td>
</tr>
<tr>
<td></td>
<td>Reactive current compensated</td>
<td>-Voltage unbalance.</td>
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<td></td>
<td>Voltage unbalance</td>
<td>-Voltage flicker.</td>
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<td></td>
<td>Voltage unbalance</td>
<td>-Voltage flicker.</td>
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4. SHUNT ACTIVE POWER FILTERS

Shunt active power filter compensate current harmonics by injecting equal-but-opposite harmonic compensating current. In this case the shunt active power filter operates as a current source injecting the harmonic components generated by the load but phase shifted by 180°. This principle is applicable to any type of load considered a harmonic source. Moreover, with an appropriate control scheme, the active power filter can also compensate the load power factor. In this way, the power distribution system sees the non linear load and the active power filter as an ideal resistor. The current compensation characteristic of the shunt active power filter is shown in Fig.1.

4.1 Power Circuit Topology

Shunt active power filters are normally implemented with pulse-width modulated voltage source inverters. In this type
of applications, the PWM-VSI operates as a current controlled voltage source. Traditionally, 2 level PWM-VSI have been used to implement such system. However, in the past years multilevel PWM voltage source inverters have been proposed to develop active power filters for medium voltage applications. Also, active power filters implemented with multiple VSI connected in parallel to a dc bus but in series through a transformer or in cascade has been proposed in the technical literature.

The use of VSI connected in cascade is an interesting alternative to compensate high power non-linear load. The use of two PWM-VSI of different rated power allows the use of different switching frequencies, reducing switching stresses and commutation losses in the overall compensation system.

In recent years, there has been an increasing interest in using multilevel inverters for high power energy conversion, especially for drives and reactive power compensation. Multilevel PWM inverters can be connected to high voltage source without a coupling transformer. The use of neutral-point-clamped (NPC) inverters allows equal voltage shearing of the series connected devices in each phase. However, the neutral point potential deviates, resulting in an excess voltage stress to either the upper or lower set of devices.

Basically, multilevel inverters have been developed for applications in high voltage ac motor drives and static var compensation. For these types of applications, the output voltage of the multilevel inverter must be able to generate an almost sinusoidal output current. In order to generate a near sinusoidal output current, the output voltage should not contain low frequency harmonic components.

For active power filter applications the three levels NPC inverter output voltage must be able to generate an output current that follows the respective reference current which contain the harmonic and reactive component required by the load. The power circuit topology of an active power filter implemented with a Neutral-Point-Clamped voltage-source inverter is shown in Fig. 3. The three levels NPC voltage-source inverter is connected in parallel through a link reactor to the power distribution system.

The block diagram of a shunt active power filter control scheme is shown in Fig. 4 and consists of a current reference generator, a dc voltage control and the inverter gating signals generator.

The current reference circuit generates the reference currents required to compensate the load current harmonics and reactive power, and also try to maintain constant the dc voltage across the two electrolytic capacitors. There are many possibilities to develop this type of control [5], [6]. Also, the compensation effectiveness of an active power filter depends on its ability to follow with a minimum error and time delay the reference signal calculated to compensated the distorted load current. Finally, the dc voltage control unit must keep the total dc bus voltage constant and equals to a given reference value. The dc voltage control is achieved by adjusting the small amount of real power absorbed by the inverter. This small amount of real power is adjusted by changing the amplitude of the fundamental component of the reference current (Figure 4).
5. SERIES ACTIVE POWER FILTERS

It is well known that series active power filters compensate current system distortion caused by non-linear loads by imposing a high impedance path to the current harmonics which forces the high frequency currents to flow through the LC passive filter connected in parallel to the load [5]. The high impedance imposed by the series active power filter is created by generating a voltage of the same magnitude and in quadrature with the source voltages through the series transformer. The block diagram of the proposed control scheme is shown in Fig. 6. Current and voltage reference waveforms are obtained by using the Instantaneous Reactive Power Theory. Voltage unbalance is compensated by calculating the negative and zero sequence fundamental components of the system voltages. These voltage components are added to the source voltages through the series transformers compensating the voltage unbalance at the load terminals. In order to reduce the amplitude of the current flowing through the neutral conductor, the zero sequence components of the line currents are calculated. In this way, it is not necessary to sense the current flowing through the neutral conductor.

5.2 Reference Signal Generator

The compensation characteristics of the series active power filter are defined mainly by the algorithm used to generate the reference signals required by the control system. These reference signals must allow current and voltage compensation with minimum time delay. Also it is important that the accuracy of the information contained in the reference signals allows the elimination of the current harmonics and voltage unbalance presents in the power system. Since the voltage and current control scheme are independent, the equations used to calculate the voltage reference signals are the following:

\[
\begin{align*}
V_{rfa} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 1 & 1 \\
1 & a & a^2 \\
1 & a^2 & a \\
\end{bmatrix} \cdot V_a \\
V_{rfb} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 1 & 1 \\
1 & a & a^2 \\
1 & a^2 & a \\
\end{bmatrix} \cdot V_b \\
V_{rfc} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 1 & 1 \\
1 & a & a^2 \\
1 & a^2 & a \\
\end{bmatrix} \cdot V_c
\end{align*}
\]

The voltages \( V_r, V_b, \) and \( V_c \) correspond to the phase to neutral voltages before the series transformer (Fig. 5). The reference voltage signals are obtained by making the positive sequence component, \( V_{raf} \) zero and then applying the inverse of the Fortescue transformation. In this way the series active power filter compensates only voltage unbalance and not voltage regulation. The reference signals for the voltage unbalance control scheme are obtained by applying the following equations:

\[
\begin{align*}
V_{rfa} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 1 & 1 \\
1 & a & a^2 \\
1 & a^2 & a \\
\end{bmatrix} \cdot V_{a0} \\
V_{rfb} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 1 & 1 \\
1 & a & a^2 \\
1 & a^2 & a \\
\end{bmatrix} \cdot V_{b0} \\
V_{rfc} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 1 & 1 \\
1 & a & a^2 \\
1 & a^2 & a \\
\end{bmatrix} \cdot V_{c0}
\end{align*}
\]

In order to compensate current harmonics generated by the non linear loads, the following equations are used (Fig. 7):

\[
\begin{align*}
i_{raf} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 0 \\
-1/2 & \sqrt{3}/2 \\
-1/2 & -\sqrt{3}/2 \\
\end{bmatrix} \cdot I_{raf} \\
i_{ref} &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1/2 & 0 \\
-1/2 & \sqrt{3}/2 \\
-1/2 & -\sqrt{3}/2 \\
\end{bmatrix} \cdot I_{ref} \\
i_f &= \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & 0 \\
-1/2 & \sqrt{3}/2 \\
-1/2 & -\sqrt{3}/2 \\
\end{bmatrix} \cdot I_f
\end{align*}
\]

Where \( i_0 \) is the fundamental zero sequence component of the line current and is calculated using the Fortescue transformation (4).

\[
i_0 = \frac{1}{\sqrt{3}} (i_a + i_b + i_c)
\]
In (3) \( p_{\text{ref}}, q_{\text{ref}}, v_a, \) and \( v_b \) are defined according with the instantaneous reactive power theory [5]. The zero sequence fundamental component of the line currents are generated by the source voltage unbalance. Since the system voltage unbalance is eliminated by compensating the negative and zero sequence components present in the source voltage, the magnitude of the fundamental component of the line currents are significantly reduced, and therefore they need not to be compensated by the current control scheme. For this reason, the fundamental component of \( i_0 \) from equation (3) is filtered, leaving only the zero sequence harmonic components of \( i_0 \) (\( i_{0e} \)), which need to be eliminated from the source line current. Finally, the general equation that defines the references of the PWM voltage-source inverter required to compensate voltage unbalance and current harmonics is the following:

\[
\begin{bmatrix}
    v_{\text{ref}} = \\
    \left. \begin{bmatrix}
    1 & 1 & 1 \\
    -1 & 1 & 0 \\
    -1 & 0 & 1 
\end{bmatrix} \right)^{T}
\end{bmatrix} \cdot \left[ \begin{bmatrix}
    i_0 \\
    i_a \\
    i_b 
\end{bmatrix} \right] - K_1 \cdot i_{0e} - K_2 \cdot \left[ \begin{bmatrix}
    1 & 1 & 0 \\
    1 & 1 & 1 \\
    1 & 0 & 1 
\end{bmatrix} \right] \cdot v_0
\]

where \( K_1 \) is the gain of the series transformer which defines the magnitude of the impedance for high frequency current components, and \( K_2 \) defines the degree of compensation for voltage unbalance, ideally \( K_2 \) equals to 1. Also, \( i_{0e} = i_0 - i_{01} \), where \( i_{01} \) is the fundamental component of \( i_0 \). The block diagram of the control scheme that generates (5) is shown in Figure 7. It is important to note that the references signals calculated with (5) allow the flow of only reactive power between the series active power filter and the compensated power system. In order to compensate voltage regulation, the positive sequence component of the line voltages must be included in (5). The compensation of voltage regulation requires generating active power from the active power filter to the power system. Since there is no active power storage element in this topology, this function cannot be achieved with the proposed scheme.

### 5.3 Gating Signal Generator

This circuit provides the gating signals of the three-phase PWM voltage-source inverter required to compensate voltage unbalance and current harmonic components. The current and voltage reference signals are added and then the amplitude of the resultant reference waveform is adjusted in order to increase the voltage utilization factor of the PWM inverter for steady state operating conditions. The gating signals of the inverter are generated by comparing the resultant reference signal with a fixed frequency triangular waveform (5 kHz).

The triangular waveform forces the inverter switching frequency to be constant.

The higher voltage utilization of the inverter is obtained if the amplitude of the resultant reference signal is adjusted for the steady state operating condition of the series active power filter. In this case, the reference current and reference voltage waveforms are smaller. If the amplitude is adjusted for transient operating conditions, the required reference signals will have a larger value, which will create a higher dc voltage in the inverter thus defining a lower voltage utilization factor for steady state operating conditions.

### 5.4 Simulated Results

The viability of the proposed series active power filter has been verified by simulation using PSpice. Relevant results are shown in Figures 8, 9, and 10. In particular, Figure 8 shows the effect of voltage compensation with the current harmonic generator circuit not working, while in Figure 9, only the current harmonic compensator scheme is operating. In Figure 10 the series active power filter is compensating voltage unbalances and current harmonic components simultaneously. The simulation circuit is compensating three single phase non controlled rectifiers, each one connected between phase to neutral. The series active power filter starts compensating at 140 ms.

![Figure 7: The block diagram of the gating Signal generator](image)

![Figure 8: Simulated waveforms for voltage unbalance compensation. Phase to neutral voltages at the load terminals before and after series compensation. (Current harmonic compensator not](image)
operating).

Figure 9: Simulated waveforms for current harmonic compensation. a) Neutral current flowing to the ac mains before and after compensation. b) Line currents flowing to the ac mains before and after compensation. (Voltage unbalance compensator not operating).

Figure 10: Simulated results for voltage unbalance and current harmonic compensation, before and after compensation. a) Ac mains neutral current. b) Phase to neutral load voltages. c) Ac source line current.

6. CONCLUSION

In this paper the performance of an active power filter (APF) depends on the inverter characteristics, applied control method, and the accuracy of the reference signal generator. The accuracy of the reference generator is the most critical item in determining the performance of APFs. This paper implemented an efficient reference signal generator composed of an improved adaptive predictive filter. The performance of the implemented reference signal generator was first verified through a simulation with MATLAB.

REFERENCES


BIOGRAPHIES

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